


Bulletproofs: applications

July 31, 2025

Distributed Lab

 zkdl-camp.github.io

 github.com/ZKDL-Camp



Plan

- 1 Introduction
- 2 IPA polynomial commitment scheme
- 3 Range proofs
- 4 Arithmetic circuits

Introduction
●○○○○

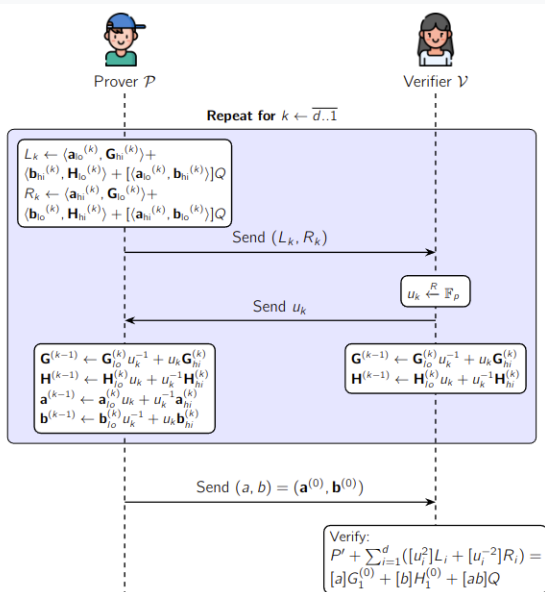
IPA polynomial commitment scheme
○○○

Range proofs
○○○○○○○○○○○○

Arithmetic circuits
○○○○○○○○○○○○○○○○○○○○

Introduction

Inner-product argument: illustration



Recap: inner-product argument

- **Goal:** Prove $\langle a, b \rangle = c$ with logarithmic proof size
- **Commitment:** $P' = \langle a, G \rangle + \langle b, H \rangle + [\langle a, b \rangle]Q$
- Protocol recursively compresses vectors at each step
- **Final check:** $P' + \sum ([u_i^2]L_i + [u_i^{-2}]R_i) = [a]G + [b]H + [ab]Q$

Key properties

Proof size is $O(\log_2 n)$, prover and verifier both run in $O(n)$. The protocol doesn't need a *trusted setup*. Protocol is *knowledge sound* and *perfect complete* but not *zero-knowledge*.

Idea

We could provide *zero-knowledge* directly to *inner-product argument* construction or use **zk-mul** protocol for outer construction.

Recap: zkmul

Consider relation $R_{mul} = \{(\perp; l(x), r(x), t(x)) \mid t(x) = l(x)r(x)\}$ where $l(x) = a + s_L x$, $r(x) = b + s_R x$, $t(x) = l(x)r(x)$. Protocol **zk-mul** is defined as follows:

- Prover computes and sends to \mathcal{V} commitments to $l(x), r(x), t(x)$:

$$\begin{aligned} A &= [a]G + [b]H + [\alpha]B & T_0 &= [ab]G + [\tau_0]B \\ S &= [s_L]G + [s_R]H + [\beta]B & T_1 &= [s_L + s_R]G + [\tau_1]B \\ & & T_2 &= [s_L s_R]G + [\tau_2]B \end{aligned}$$

- Verifier draws random challenge $u \in \mathbb{F}_p$ and sends it to prover
- Prover evaluates and sends to Verifier $(l_u, r_u, t_u, \alpha_u, \tau_u)$:

$$l_u = l(u), r_u = r(u), t_u = l_u \cdot r_u, \alpha_u = \alpha + \beta u, \tau_u = \tau_0 + \tau_1 u + \tau_2 u^2$$

- Verifier checks: $A + [u]S \stackrel{?}{=} [l_u]G + [r_u]H + [\alpha_u]B$,
 $[t_u]G + [\tau_u]B \stackrel{?}{=} T_0 + [u]T_1 + [u^2]T_2, t_u \stackrel{?}{=} l_u r_u$

What's next?



IPA polynomial commitment scheme

Recap: Polynomial commitments

- Polynomial commitment scheme

$$\mathcal{C} = (\text{Setup}, \text{Commit}, \text{Open}, \text{VerifyOpen})$$

allows to commit to a polynomial $f(x) = \sum_{i=0}^{n-1} a_i x^i$ and prove its evaluation at some point.

- **Applications:** SNARKs compiled with IOP + polynomial commitment scheme framework (e.g., Halo, Nova, Spartan, Plonk)
- **Desirable properties:** sublinear size, efficient, no trusted setup

Example

One famous example is the **KZG** polynomial commitment scheme, which uses bilinear pairings and requires a trusted setup.

IPA polynomial commitment

Let $f(x) = \sum_{i=0}^{n-1} a_i x^i$ be a polynomial of degree $n - 1 = 2^d - 1$.

The **non-hiding IPA polynomial commitment scheme** $\mathcal{C}_{ip} = (\text{Setup}, \text{Commit}, \text{Open}, \text{VerifyOpen})$ is defined as follows:

- **Setup** returns independent generators $G = (G_1, \dots, G_n)$.
- **Commit** returns $\text{Com}(f) = \langle f, G \rangle$ where $f = (a_0, \dots, a_{n-1})$
- **Open** given evaluation point $u \in \mathbb{F}_p$ computes $u^n = (1, u, u^2, \dots, u^{n-1})$, obtains $f(u) = \langle f, u^n \rangle$ and runs *inner-product argument* Π_{ip} non-interactively setting

$$a = f, b = u^n, P = \text{Com}(f), c = f(u)$$

to produce an evaluation proof π_{ip}

- **VerifyOpen** given evaluation point $u \in \mathbb{F}_p$ and commitment $\text{Com}(f)$ validates proof π_{ip} running the non-interactive verifier \mathcal{V} of *inner-product argument*.

Range proofs

Range proofs: motivation

- **Goal:** Prove $v \in [0, 2^n)$ without revealing v :

$$\mathcal{R}_{rp} = \{(G, B, V, n; v, \gamma) \mid V = [v]G + [\gamma]B, v \in [0, 2^n)\}$$

- **Applications:** Confidential transactions (e.g., Monero, Mumblewimble), other privacy-preserving protocols.
- **Idea:** Prove $v = \sum_{i=0}^{n-1} v_i 2^i$ and $\forall i \in 0..n-1 : v_i \in \{0, 1\}$

Naive approach

One could prove $v = \sum_{i=0}^{n-1} v_i 2^i$ and $\forall i \in 0..n-1 : v_i \in \{0, 1\}$ using Σ -protocols, but this would be inefficient due to linear proof.

Compiling range proof into inner-product

Firstly, write v in base-2 representation: $v = \sum_{i=0}^{\lfloor \log_2 v \rfloor} 2^i v_i$ and $\mathbf{a}_L = (v_0, v_1, \dots, v_{n-1})$ be the vector of bits padded with zeroes to length n , define $\mathbf{a}_R = \mathbf{a}_L - \mathbf{1}^n$ so the range validation that v lays in $[0, 2^n)$ implies two checks:

- The following inner-product equality holds: $\langle \mathbf{a}_L, \mathbf{2}^n \rangle = v$
- Each bit v_i must be either 0 or 1:

$$\mathbf{a}_L - \mathbf{a}_R - \mathbf{1}^n = \mathbf{0}^n$$

$$\mathbf{a}_L \circ \mathbf{a}_R = \mathbf{0}^n$$

Example

Let $\mathbf{a}_L = (1, 0, 1, 0)$, $\mathbf{a}_R = (0, -1, 0, -1)$, then $\mathbf{a}_L \circ \mathbf{a}_R = (0, 0, 0, 0)$

Compiling range proof into inner-product

This two checks imply verification that some vector is zero vector, for that we use some challenge $y \in \mathbb{F}_p$ and check inner-product equalities

$$\langle a_L \circ a_R, y^n \rangle = 0 \text{ and } \langle a_L - a_R - 1^n, y^n \rangle = 0$$

This checks are sound because the prover doesn't know challenge y in advance. So we must combine three inner-product checks:

1. $\langle a_L, 2^n \rangle = v$
2. $\langle a_L, a_R \circ y^n \rangle = 0$
3. $\langle a_L - a_R - 1^n, y^n \rangle = 0$

into one soundly summing up with powers of other challenge $z \in \mathbb{F}_p$:

$$z^2 \cdot \langle a_L, 2^n \rangle + z \cdot \langle a_L - a_R - 1^n, y^n \rangle + \langle a_L, a_R \circ y^n \rangle = z^2 v$$

Compiling range proof into inner-product

Using some dark linear algebra wizardry we could combine the three inner-product checks into a single one inner-product check:

$$\langle a_L - z \cdot 1^n, z^2 \cdot 2^n + z \cdot y^n + a_R \circ y^n \rangle = z^2 v + \delta(y, z)$$

Where $\delta(y, z)$ could easily be computed by verifier:

$$\delta(y, z) = (z - z^2) \langle 1^n, y^n \rangle - z^3 \langle 1^n, 2^n \rangle$$

Now it's time to bring out **zk-mul** for inner-products!

Firstly, construct the blinding polynomials for a_L and a_R :

$$a'_L \leftarrow a_L + s_L x \quad a'_R \leftarrow a_R + s_R x$$

Compute polynomials $l(x) = l_0 + l_1 x$, $r(x) = r_0 + r_1 x$:

$$l(x) = a'_L - z \cdot 1^n = (a_L + s_L x) - z \cdot 1^n = a_L - z \cdot 1^n + s_L x$$

$$\begin{aligned} r(x) &= z^2 \cdot 2^n + z \cdot y^n + a'_R \circ y^n = z^2 \cdot 2^n + z \cdot y^n + (a_R + s_R x) \circ y^n \\ &= z^2 \cdot 2^n + z \cdot y^n + a_R \circ y^n + s_R \circ y^n x \end{aligned}$$

Compiling range proof into inner-product

$$t(x) = \langle l(x), r(x) \rangle = t_0 + t_1x + t_2x^2$$

Now \mathcal{P} needs to apply **zk-mul** for proving:

$$t_0 = \langle a_L - z \cdot 1^n, z^2 \cdot 2^n + z \cdot y^n + a_R \circ y^n \rangle = z^2v + \delta(y, z)$$

Note: \mathcal{V} could compute commitment $Com(t_0)$ using $V = Com(v)$

Remark

We couldn't apply raw **zk-mul** as l_0 depends on verifier-provided challenges, instead \mathcal{P} firstly commits to a_L, a_R and blinders s_L, s_R , obtains challenges y, z from \mathcal{V} and computes rest of the commitments.

During verification phase \mathcal{V} should adjust commitments to $l(x), r(x)$ by himself using homomorphic properties of Pedersen commitment scheme.

Range proofs: building the protocol

- Setup returns independent generators $G, H \in \mathbb{G}^n$
- Prover does bit decomposition of v : $a_L \leftarrow v, a_R \leftarrow a_L - 1^n$,
chooses blinding terms $s_L, s_R \in \mathbb{F}_p^n, \alpha, \beta \in \mathbb{F}_p$, sends commitments:

$$A = \langle a_L, G \rangle + \langle a_R, H \rangle + [\alpha]B \quad S = \langle s_L, G \rangle + \langle s_R, H \rangle + [\beta]B$$

- Verifier \mathcal{V} samples challenges $y, z \xleftarrow{R} \mathbb{F}_p$ and sends them to \mathcal{P}
- Prover \mathcal{P} reconstructs polynomials $l(x), r(x), t(x)$:

$$l(x) = a_L - z \cdot 1^n + s_L x$$

$$r(x) = z^2 \cdot 2^n + z \cdot y^n + a_R \circ y^n + s_R \circ y^n x$$

$$t(x) = \langle l(x), r(x) \rangle = t_0 + t_1 x + t_2 x^2$$

$$t_0 = \langle a_L - z \cdot 1^n, z^2 \cdot 2^n + z \cdot y^n + a_R \circ y^n \rangle = z^2 v + \delta(y, z)$$

$$t_1 = \langle a_L - z \cdot 1^n, y^n \circ s_R \rangle + \langle y^n \circ (a_R + z \cdot 1^n) + z^2 \cdot 2^n, s_L \rangle$$

$$t_2 = \langle s_L, y^n \circ s_R \rangle$$

Range proofs: proving

- Prover \mathcal{P} draws blinding factors $\tau_1, \tau_2 \xleftarrow{R} \mathbb{F}_p$ and sends to \mathcal{V} commitments for coefficients of $t(x)$:

$$T_1 = [t_1]G + [\tau_1]B$$

$$T_2 = [t_2]G + [\tau_2]B$$

Note: prover does not have to send commitment to t_0 as it's the inner-product we want to prove and it could be computed from high-level commitment V .

- Verifier \mathcal{V} samples and sends to \mathcal{P} evaluation point $u \xleftarrow{R} \mathbb{F}_p$
- Prover \mathcal{P} evaluates polynomials at u :

$$l_u = l(u)$$

$$\alpha_u = \alpha + \beta u$$

$$r_u = r(u)$$

$$\tau_u = z^2\gamma + \tau_1 u + \tau_2 u^2$$

$$t_u = t(u) = t_0 + t_1 u + t_2 u^2$$

and sends $(l_u, r_u, t_u, \alpha_u, \tau_u)$ to \mathcal{V} .

Range proofs: verification

- Verifier \mathcal{V} checks:

$$\begin{aligned}
 A + [u]S + \langle -z \cdot 1^n, G \rangle + \langle z \cdot y^n + z^2 \cdot 2^n, y^{-n} \circ H \rangle \\
 &\stackrel{?}{=} \langle l_u, G \rangle + \langle r_u, y^{-n} \circ H \rangle + [\alpha_u]B \\
 [t_u]G + [\tau_u]B &\stackrel{?}{=} [z^2]V + [\delta(y, z)]G + [u]T_1 + [u^2]T_2 \\
 t_u &\stackrel{?}{=} \langle l_u r_u \rangle
 \end{aligned}$$

Remark

To provide logarithmic size-proof instead of sending l_u, r_u parties could run an inner-product argument **IPA** on inputs $(G, y^{-n} \circ H, P, t_u; l_u, r_u)$ where:

$$P = A + [u]S + \langle -z \cdot 1^n, G \rangle + \langle z \cdot y^n + z^2 \cdot 2^n, y^{-n} \circ H \rangle - [\alpha_u]B$$

Range proofs: efficiency & extensions

Theorem

*The **range proof protocol** Π_{rp} has perfect completeness, computational extended witness emulation, perfect honest-verifier zero-knowledge*

Note that protocol is efficient as it has logarithmic proof size.

Remark

The **range proof protocol** could be extended to support proving multiple range proofs at once with some efficiency improvements.

Range proofs & subset-sum NP-complete problem

One of the most famous *NP-complete* problems is the **subset-sum problem**: given a set of numbers presented as vector s and number $v \in \mathbb{N}$, does a some subset sums up to v . It turns out that we could use our **range-proof** protocol for this problem. One could simply replace first inner-product check $\langle a_L, 2^n \rangle = v$ with $\langle a_L, s \rangle = v$ where a_L is the secret vector of bits that encode positions of s that sum up to v .

Example

Let $s = (6, 8, 2, 3)$ and $v = 14$. Then setting $a_L = (1, 1, 0, 0)$ we could use Π_{rp} to prove that there exists a subset of s that sums up to $v = 14$ without disclosing that subset.

Therefore, **bulletproofs range proof** protocol is capable to prove a knowledge of witness to any *NP*-problem as they all could be reduced to the **subset-sum problem**

Arithmetic circuits

Bulletproofs for arithmetic circuits

- **Goal:** Prove that a circuit computes correctly without revealing inputs or intermediate values (*circuit satisfiability problem*).
- **Approach:** Use inner-product argument to prove correctness of arithmetic circuits
- **Applications:** Privacy-preserving smart contracts, confidential computations, zero-knowledge proofs for complex computations

Bulletproofs arithmetization slightly differs from the classic R1CS, however it could be transformed vice-versa easily. Also **bulletproofs** arithmetization is more convenient and human-friendly for encoding most of the arithmetic circuits than the R1CS.

Arithmetic circuits: variables

There is two types of variables in *bulletproofs* constraint system:

- **High-level variables** $v \in \mathbb{F}_p^m$ are the private witness inputs to the circuit, typically provided with Pedersen commitments $V \in \mathbb{G}^m$.
- **Low-level variables** $a_L, a_R, a_O \in \mathbb{F}_p^n$ are the intermediate witness values of computation.

We will define circuit as a set of multiplication constraints operating with *low-level* variables and set of linear constraints which links *low-level* variables between each other and *high-level* variables as well.

Arithmetic circuits: constraints

Multiplication constraints are defined with one vector equation:

$$a_L \circ a_R = a_O$$

Linear constraints are defined via:

$$W_L \cdot a_L + W_R \cdot a_R + W_O \cdot a_O = W_V \cdot v + c$$

Where a_L, a_R, a_O – vectors of left and right inputs for multiplication gates and output values (all of them are low-level variables).

$W_L, W_R, W_O \in \mathbb{F}_p^{q \times n}, W_V \in \mathbb{F}_p^{q \times m}$ – public matrices of weights for linear constraints (obviously known to verifier). $c \in \mathbb{F}_p^q$ – public vector of constants. Typically they encode wiring of the circuit and other linear relations between variables.

Arithmetic circuits: example

Example

Consider the following elliptic curve membership circuit. Here witness (v_1, v_2) should satisfy elliptic curve equation:

$$y^2 = x^3 + ax + b$$

The arithmetization for this circuit is as follows:

Low-level variables:

$$a_L = \begin{bmatrix} x \\ x \\ y \end{bmatrix}, \quad a_R = \begin{bmatrix} x \\ x^2 \\ y \end{bmatrix}, \quad a_O = \begin{bmatrix} x^2 \\ x^3 \\ y^2 \end{bmatrix}$$

High-level variables:

$$v = \begin{bmatrix} v_1 \\ v_2 \end{bmatrix}$$

Arithmetic circuits: example

Example

Multiplication constraints:

$$a_L \circ a_R = a_O \Rightarrow \begin{bmatrix} x \cdot x = x^2 \\ x \cdot x^2 = x^3 \\ y \cdot y = y^2 \end{bmatrix}$$

Linear constraints:

$$\begin{aligned} a_L^{(1)} &= v_1 & a_R^{(1)} &= v_1 \\ a_L^{(2)} - a_L^{(1)} &= 0 & a_R^{(2)} - a_O^{(1)} &= 0 \\ a_L^{(3)} &= v_2 & a_R^{(3)} &= v_2 \\ a_O^{(3)} - a_O^{(2)} - a \cdot a_L^{(1)} &= b \end{aligned}$$

$$W_L \cdot a_L + W_R \cdot a_R + W_O \cdot a_O = W_V \cdot v + c$$

Arithmetic circuits: example

Example

$$W_L = \begin{bmatrix} 1 & 0 & 0 \\ 0 & 0 & 0 \\ -1 & 1 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & 0 \\ -a & 0 & 0 \end{bmatrix}, \quad W_R = \begin{bmatrix} 0 & 0 & 0 \\ 1 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 1 \\ 0 & 0 & 0 \end{bmatrix}, \quad W_O = \begin{bmatrix} 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 0 \\ -1 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & 0 & 0 \\ 0 & -1 & 1 \end{bmatrix},$$
$$W_V = \begin{bmatrix} 1 & 0 \\ 1 & 0 \\ 0 & 0 \\ 0 & 0 \\ 0 & 1 \\ 0 & 1 \\ 0 & 0 \end{bmatrix}, \quad c = \begin{bmatrix} 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ 0 \\ b \end{bmatrix}$$

Bulletproofs for circuits: relation

Consider the relation:

$$\mathcal{R}_{sat} = \left\{ \begin{array}{l} (G, B, V, W_L, W_R, W_O, W_V, c; a_L, a_R, a_O, v, r) \mid \\ \forall i = 1..m : V_i = [v_i]G + [r_i]B \wedge \\ a_L \circ a_R = a_O \wedge \\ W_L \cdot a_L + W_R \cdot a_R + W_O \cdot a_O = W_V \cdot v + c \end{array} \right\}$$

Where $a_L, a_R, a_O \in \mathbb{F}_p^n$, $v, r \in \mathbb{F}_p^m$, $W_L, W_R, W_O \in \mathbb{F}_p^{q \times n}$,
 $W_V \in \mathbb{F}_p^{q \times m}$, $c \in \mathbb{F}_p^q$.

Note

Informally this relation states that there exists a valid witness v that satisfies all constraints of the circuit. For the verifier witness is presented only as commitments vector V .

Arithmetic circuits: compiling into inner-product

We could use similar to *range-proofs* technique to compile constraints of the circuit into inner-product relation. For multiplicative constraints take random $y \in \mathbb{F}_p$ and apply zero check:

$$\langle a_L \circ a_R - a_O, y^n \rangle = 0$$

Same for linear constraints, but for different randomness $z \in \mathbb{F}_p$:

$$\langle W_L \cdot a_L + W_R \cdot a_R + W_O \cdot a_O - W_V \cdot v - c, z^q \rangle = 0$$

Combine this two checks to one using the same randomness z :

$$\langle a_L \circ a_R - a_O, y^n \rangle + \langle z \cdot z^q, W_L \cdot a_L + W_R \cdot a_R + W_O \cdot a_O - W_V \cdot v - c \rangle = 0$$

This check is sound as typically a prover could not control values of y, z before he commits to a_L, a_R, a_O and v .

Arithmetic circuits: compiling into inner-product

Denote $w_c = \langle z \cdot z^q, c \rangle$ and flattened linear constraints (still public and easily computed by verifier):

$$w_L = W_L^T \cdot (z \cdot z^q) \quad w_R = W_R^T \cdot (z \cdot z^q)$$

$$w_O = W_O^T \cdot (z \cdot z^q) \quad w_V = W_V^T \cdot (z \cdot z^q)$$

Again doing some linear algebra witchcraft we could separate a_L, a_O to be on the left side of the inner-product and a_R to be on the right:

$$w_c + \langle w_V, v \rangle + \delta(y, z) = \\ \langle a_L + y^{-n} \circ w_R, y^n \circ a_R + w_L \rangle + \langle a_O, -y^n + w_O \rangle$$

Where $\delta(y, z) = \langle y^{-n} \circ w_R, w_L \rangle$ – easily computable by \mathcal{V} .

Here we have a sum of 2 separate inner-products, we could express it as second-degree coefficient of the following polynomial:

$$\langle ax + cx^2, d + bx \rangle = s_1x + s_2x^2 + s_3x^3 = \\ x \cdot \langle a, d \rangle + x^2 \cdot (\langle a, b \rangle + \langle c, d \rangle) + x^3 \cdot \langle c, b \rangle$$

Arithmetic circuits: compiling into inner-product

$$\begin{aligned} a &\leftarrow a_L + y^{-n} \circ w_R & b &\leftarrow y^n \circ a_R + w_L \\ c &\leftarrow a_O & d &\leftarrow -y^n + w_O \end{aligned}$$

Desired sum of inner products is the second-degree coefficient s_2 :

$$w_c + \langle w_V, v \rangle + \delta(y, z) = s_2$$

To obtain final polynomials $l(x), r(x)$ we must firstly blind a_L, a_R :

$$a_L \leftarrow a_L + s_L x^2 \quad a_R \leftarrow a_R + s_R x^2$$

And finally compute polynomials $l(x), r(x)$ as follows:

$$\begin{aligned} l(x) &= s_L \cdot x^3 + a_O \cdot x^2 + (a_L + y^{-n} \circ w_R) \cdot x \\ r(x) &= y^n \circ s_R \cdot x^3 + (y^n \circ a_R + w_L) \cdot x - y^n + w_O \\ t(x) &= \langle l(x), r(x) \rangle = \sum_{i=0}^6 t_i x_i \end{aligned}$$

Where $t_2 = w_c + \langle w_V, v \rangle + \delta(y, z)$ – desired sum of inner-products.

Arithmetic circuits: witness commitments

Here we could again apply modified **zk-mul** to prove that t_2 is a valid sum of inner-products:

- Setup: returns vectors of independent generators $G, H \in \mathbb{G}^n$.
- Prover \mathcal{P} choses blinding factors $\alpha, \beta, \gamma \in \mathbb{F}_p, s_L, s_R \in \mathbb{F}_p^n$ and sends the following commitments to \mathcal{V} :

$$\begin{aligned}A_I &= \langle a_L, G \rangle + \langle a_R, H \rangle + [\alpha]B \\A_O &= \langle a_O, G \rangle + [\gamma]B \\S &= \langle s_L, G \rangle + \langle s_R, H \rangle + [\beta]B\end{aligned}$$

- Verifier samples challenges $y, z \xleftarrow{R} \mathbb{F}_p$ and sends them to \mathcal{P} .

Arithmetic circuits: product commitments

- Using challenges y, z prover forms polynomials $l(x), r(x), t(x)$:

$$l(x) = s_L \cdot x^3 + a_O \cdot x^2 + (a_L + y^{-n} \circ w_R) \cdot x$$

$$r(x) = y^n \circ s_R \cdot x^3 + (y^n \circ a_R + w_L) \cdot x - y^n + w_O$$

$$t(x) = \langle l(x), r(x) \rangle = t_1x + t_2x^2 + t_3x^3 + t_4x^4 + t_5x^5 + t_6x^6$$

\mathcal{P} chooses random blinding factors $\tau_1, \tau_3, \tau_4, \tau_5, \tau_6 \in \mathbb{F}_p$ and sends to \mathcal{V} commitments to its coefficients:

$$T_1 = [t_1]G + [\tau_1]B \quad T_3 = [t_3]G + [\tau_3]B \quad T_4 = [t_4]G + [\tau_4]B$$

$$T_5 = [t_5]G + [\tau_5]B \quad T_6 = [t_6]G + [\tau_6]B$$

Note: Prover does not send separate commitment to t_2 as the verifier could derive it from V and the circuit public parameters:

$$t_2 = w_c + \langle w_V, v \rangle + \delta(y, z)$$

$$T_2 = \langle w_V, V \rangle + [\delta(y, z) + w_c]G$$

Arithmetic circuits: evaluating polynomials

- Verifier samples and sends to \mathcal{P} random evaluation point $u \xleftarrow{R} \mathbb{F}_p$.
- Prover evaluates polynomials at u :

$$l_u = l(u)$$

$$r_u = r(u)$$

$$t_u = \langle l_u, r_u \rangle = t(u)$$

$$\tau_u = \tau_1 \cdot u + \langle w_V, r \rangle u^2 + \tau_3 \cdot u^3 + \tau_4 \cdot u^4 + \tau_5 \cdot u^5 + \tau_6 \cdot u^6$$

$$\alpha_u = \alpha u + \gamma u^2 + \beta u^3$$

and sends $(l_u, r_u, t_u, \alpha_u, \tau_u)$ to \mathcal{V} .

Arithmetic circuits: verification

- Verifier performs checks:

$$\begin{aligned}
 & [u]A_I + [u^2]A_O + [u^3]S - \langle 1, H \rangle + \\
 & u \cdot (\langle y^{-n} \circ w_L, G \rangle + \langle y^{-n} \circ w_R, H \rangle) + \langle y^{-n} \circ w_O, H \rangle \\
 & \stackrel{?}{=} \langle l_u, G \rangle + \langle r_u, y^{-n} \circ H \rangle + [\alpha_u]B
 \end{aligned}$$

$$\begin{aligned}
 [t_u]G + [\tau_u]B & \stackrel{?}{=} [u]T_1 + u^2 \cdot (\langle w_V, V \rangle + [\delta(y, z) + w_c]G) + \\
 & [u^3]T_3 + [u^4]T_4 + [u^5]T_5 + [u^6]T_6 \\
 t_u & \stackrel{?}{=} \langle l_u, r_u \rangle
 \end{aligned}$$

Remark

To provide logarithmic proof instead of sending l_u, r_u parties could run **IPA** on inputs $(G, y^{-n} \circ H, P, t_u; l_u, r_u)$ where:

$$\begin{aligned}
 P = & [u]A_I + [u^2]A_O + [u^3]S - \langle 1, H \rangle + \\
 & u \cdot (\langle y^{-n} \circ w_L, G \rangle + \langle y^{-n} \circ w_R, H \rangle) + \langle y^{-n} \circ w_O, H \rangle - [\alpha_u]B
 \end{aligned}$$

Arithmetic circuits: efficiency & extensions

Theorem

The *arithmetic circuits protocol* has perfect completeness, computational extended witness emulation, perfect honest-verifier zero-knowledge

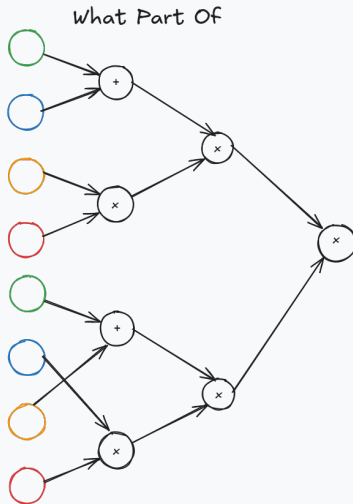
The protocol is efficient as it has logarithmic proof size.

Remark

The **arithmetic circuits protocol** could be slightly modified to provide intermediate random challenges inside the circuit. For example it would allow proving *permutation check*:

$\{a, b\} = \{c, d\} \iff (a - x) \cdot (b - x) = (c - x) \cdot (d - x)$ for some random challenge x .

Questions?



You don't understand?